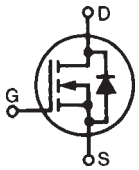
IXYS

PolarHT™ HiPerFET Power MOSFET

IXFK 120N25P
IXFX 120N25P

V_{DSS} = 250 V
I_{D25} = 120 A
$R_{DS(on)}$ ≤ 24 mΩ
t_{rr} ≤ 200 ns

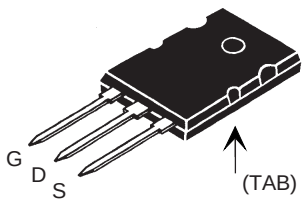
N-Channel Enhancement Mode
Fast Intrinsic Diode
Avalanche Rated



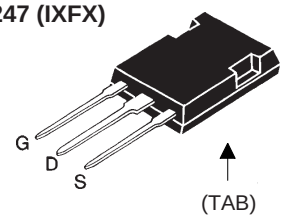
Symbol	Test Conditions	Maximum Ratings	
V_{DSS}	T_J = 25°C to 175°C	250	V
V_{DGR}	T_J = 25°C to 175°C; R_{GS} = 1 MΩ	250	V
V_{GS}	Continuous	±20	V
V_{GSM}	Transient	±30	V
I_{D25}	T_C = 25°C	120	A
$I_{D(RMS)}$	External lead current limit	75	A
I_{DM}	T_C = 25°C, pulse width limited by T_{JM}	300	A
I_{AR}	T_C = 25°C	60	A
E_{AR}	T_C = 25°C	60	mJ
E_{AS}	T_C = 25°C	2.5	J
dv/dt	$I_S \le I_{DM}$, di/dt ≤ 100 A/μs, $V_{DD} \le V_{DSS}$, $T_J \le 150°C$, R_G = 4 Ω	10	V/ns
P_D	T_C = 25°C	700	W
T_J		-55 ... +175	°C
T_{JM}		175	°C
T_{stg}		-55 ... +150	°C
T_L	1.6 mm (0.062 in.) from case for 10 s	300	°C
T_{SOLD}	Plastic body for 10 s	260	°C
M_d	Mounting torque	1.13/10	Nm/lb.in.
Weight	TO-264	10	g
	PLUS247	6	g

Symbol	Test Conditions (T_J = 25°C, unless otherwise specified)	Characteristic Values Min.	Typ.	Max.	
BV_{DSS}	V_{GS} = 0 V, I_D = 250 μA	250			V
$V_{GS(th)}$	$V_{DS} = V_{GS}$, I_D = 4 mA	2.5		5.0	V
I_{GSS}	V_{GS} = ±20 V, V_{DS} = 0 V			±200	nA
I_{DSS}	$V_{DS} = V_{DSS}$			25	μA
	V_{GS} = 0 V, T_J = 125°C			250	μA
$R_{DS(on)}$	V_{GS} = 10 V, $I_D = 0.5\ I_{D25}$ Pulse test, t ≤ 300 μs, duty cycle d ≤ 2 %		19	24	mΩ

TO-264 (IXFK)



PLUS247 (IXFX)



G = Gate D = Drain
S = Source TAB = Drain

Features

- International standard packages
- Unclamped Inductive Switching (UIS) rated
- Low package inductance
  - easy to drive and to protect

Advantages

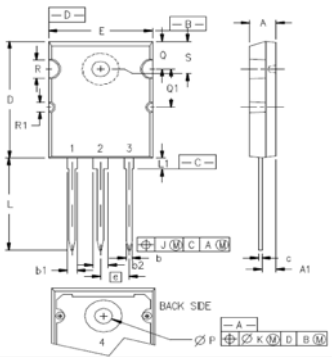
- Easy to mount
- Space savings
- High power density

Symbol	Test Conditions (T_J = 25°C, unless otherwise specified)	Characteristic Values Min.	Typ.	Max.	
g_{fs}	V_{DS}= 10 V; I_D = 0.5 I_{D25}, pulse test	45	70		S
C_{iss}	V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz		8000		pF
C_{oss}			1300		pF
C_{rss}			220		pF
$t_{d(on)}$	V_{GS} = 10 V, V_{DS} = 0.5 V_{DSS}, I_D = 60 A R_G = 3.3 Ω (External)		30		ns
t_r			33		ns
$t_{d(off)}$			130		ns
t_f			33		ns
$Q_{g(on)}$	V_{GS}= 10 V, V_{DS} = 0.5 V_{DSS}, I_D = 0.5 I_{D25}		185		nC
Q_{gs}			50		nC
Q_{gd}			80		nC
R_{thJC}					0.18°C/W
R_{thCK}			0.15		°C/W

Source-Drain Diode

Symbol	Test Conditions (T_J = 25°C, unless otherwise specified)	Characteristic Values Min.	Typ.	Max.	
I_S	V_{GS} = 0 V			120	A
I_{SM}	Repetitive			300	A
V_{SD}	I_F = I_S, V_{GS} = 0 V, Pulse test, t ≤ 300 µs, duty cycle d ≤ 2 %			1.5	V
t_{rr}	I_F = 25 A; -di/dt = 100 A/µs; V_R = 100 V			200	ns
Q_{RM}	V_{GS} = 0 V		0.8		µC
I_{RM}			8		A

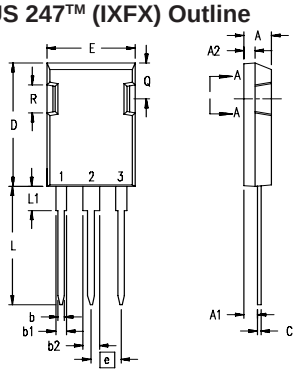
TO-264 (IXFK) Outline



1 – GATE
2, 4 – DRAIN (COLLECTOR)
3 – SOURCE (EMITTER)

SYM	INCHES MIN	INCHES MAX	MILLIMETERS MIN	MILLIMETERS MAX
A	.185	.209	4.70	5.31
A1	.102	.118	2.59	3.00
b	.037	.055	0.94	1.40
b1	.087	.102	2.21	2.59
b2	.110	.126	2.79	3.20
c	.017	.029	0.43	0.74
D	1.007	1.047	25.58	26.59
E	.760	.799	19.30	20.29
e	.215BSC		5.46 BSC	
J	.000	.010	0.00	0.25
K	.000	.010	0.00	0.25
L	.779	.842	19.79	21.39
L1	.087	.102	2.21	2.59
ØP	.122	.138	3.10	3.51
Q	.240	.256	6.10	6.50
Q1	.330	.346	8.38	8.79
ØR	.155	.187	3.94	4.75
ØR1	.085	.093	2.16	2.36
S	.243	.253	6.17	6.43

PLUS 247™ (IXFX) Outline



Terminals: 1 - Gate
2 - Drain (Collector)
3 - Source (Emitter)
4 - Drain (Collector)

Dim.	Millimeter Min.	Millimeter Max.	Inches Min.	Inches Max.
A	4.83	5.21	.190	.205
A_1	2.29	2.54	.090	.100
A_2	1.91	2.16	.075	.085
b	1.14	1.40	.045	.055
b_1	1.91	2.13	.075	.084
b_2	2.92	3.12	.115	.123
C	0.61	0.80	.024	.031
D	20.80	21.34	.819	.840
E	15.75	16.13	.620	.635
e	5.45 BSC		.215 BSC	
L	19.81	20.32	.780	.800
L1	3.81	4.32	.150	.170
Q	5.59	6.20	.220	0.244
R	4.32	4.83	.170	.190

IXYS reserves the right to change limits, test conditions, and dimensions.

Fig. 1. Output Characteristics @ 25°C

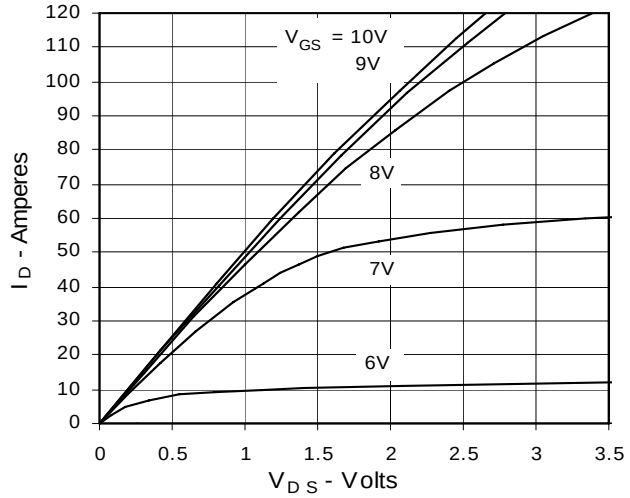


Fig. 2. Extended Output Characteristics @ 25°C

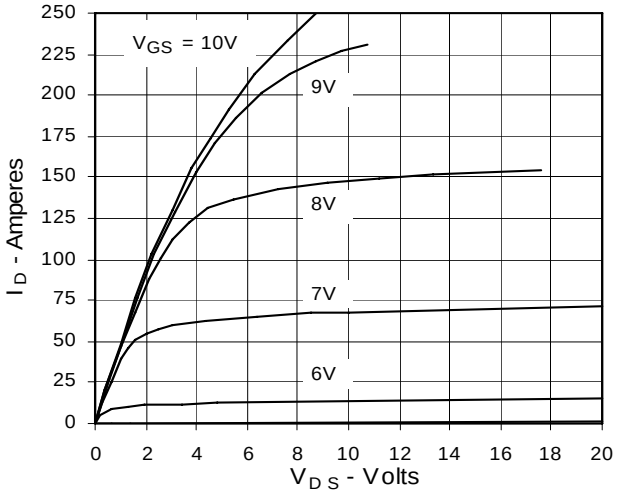


Fig. 3. Output Characteristics @ 150°C

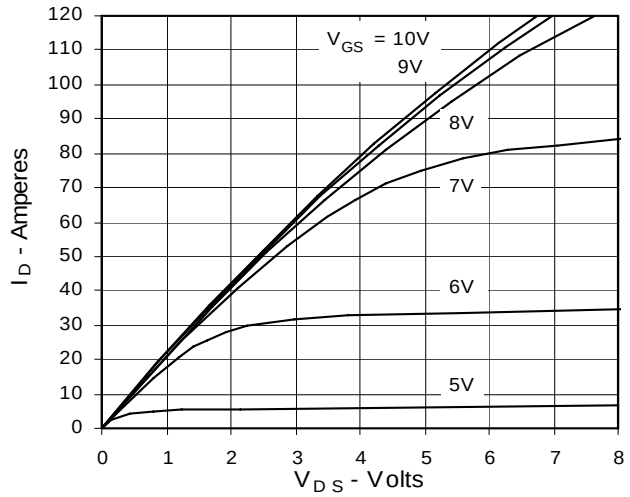


Fig. 4. $R_{DS(on)}$ Normalized to 0.5 I_{D25} Value vs. Junction Temperature

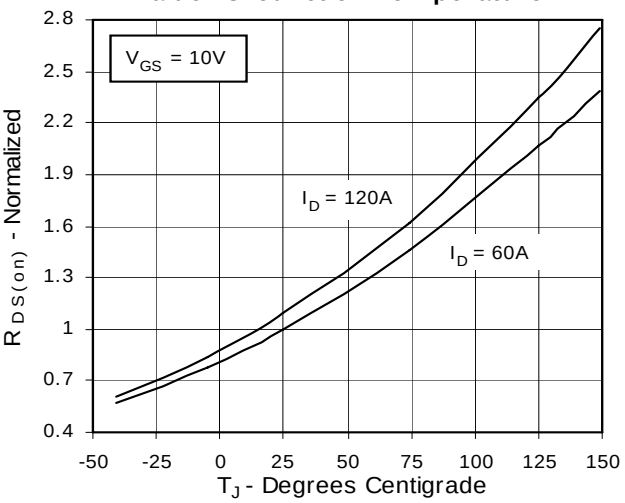


Fig. 5. $R_{DS(on)}$ Normalized to 0.5 I_{D25} Value vs. Drain Current

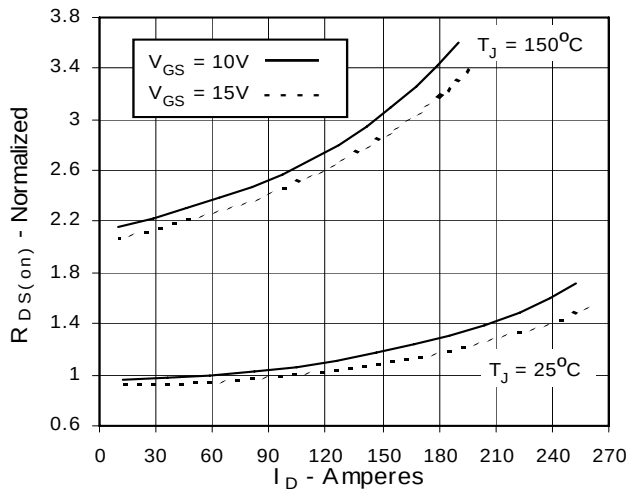


Fig. 6. Drain Current vs. Case Temperature

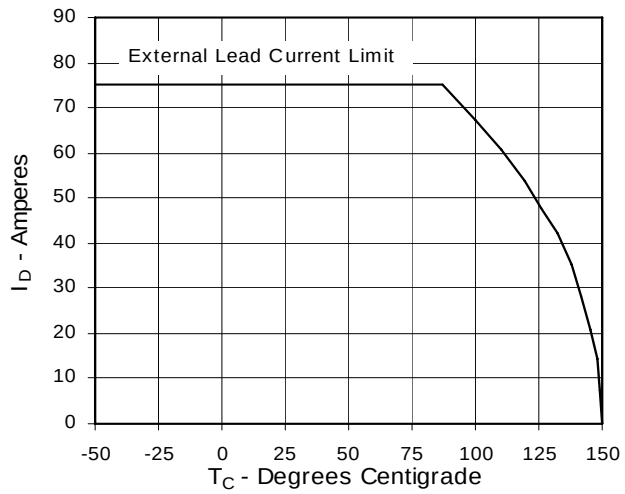


Fig. 7. Input Admittance

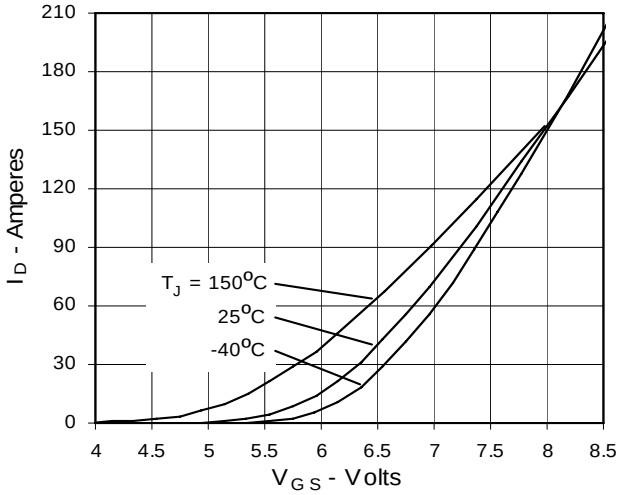


Fig. 8. Transconductance

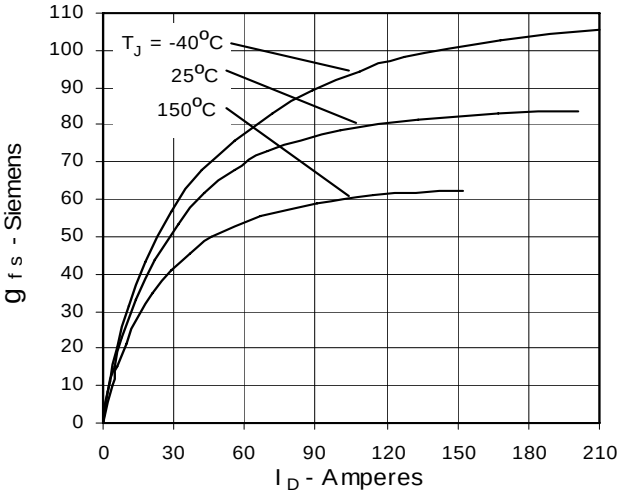


Fig. 9. Source Current vs. Source-To-Drain Voltage

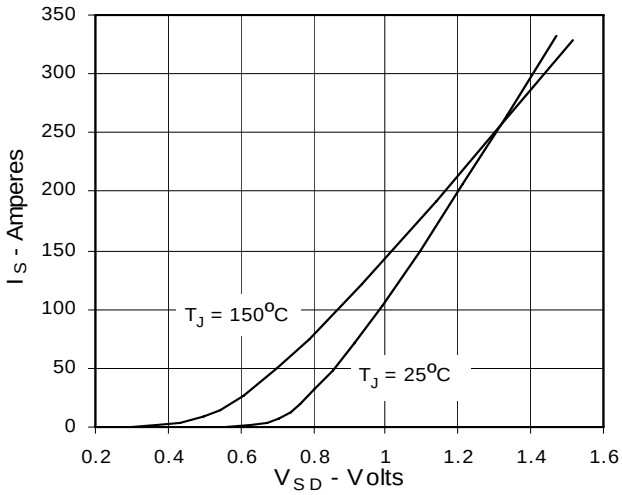


Fig. 10. Gate Charge

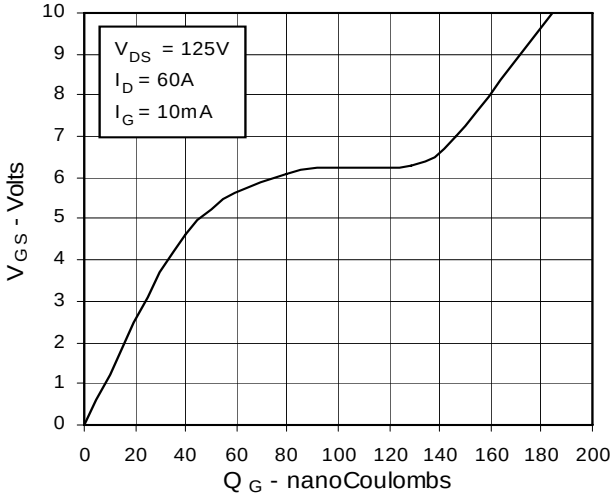


Fig. 11. Capacitance

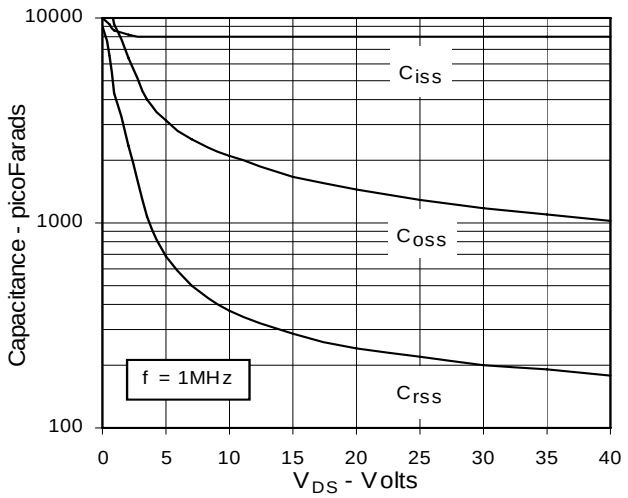


Fig. 12. Forward-Bias Safe Operating Area

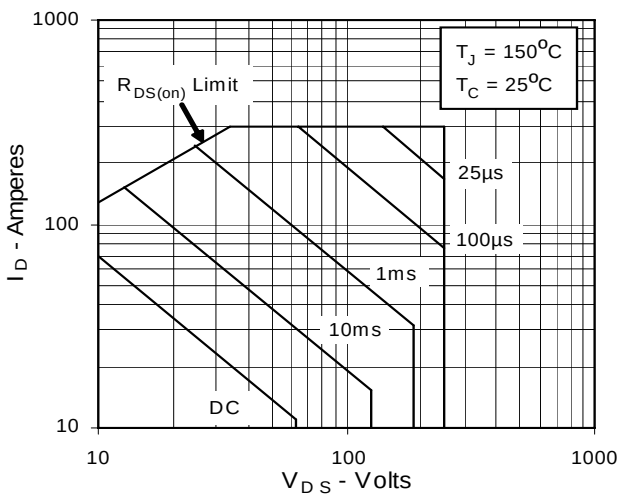


Fig. 13. Maximum Transient Thermal Resistance