


PCN Number:	20190305001.0	PCN Date:	March 06, 2019
Title:	Datasheet for ADS8681, ADS8685, ADS8689		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process
Notification Details			
Description of Change:			
<p>Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.</p>			
		ADS8681, ADS8685, ADS8689 <small>SBAS633C – FEBRUARY 2016 – REVISED OCTOBER 2018</small>	
Changes from Revision B (December 2016) to Revision C			
			Page
<ul style="list-style-type: none"> • Deleted <i>per channel</i> from <i>ALARM</i> → <i>High, Low Threshold</i> bullet in <i>Features</i> section..... • Added pin 1 indicator to RUM pinout drawing..... • Deleted <i>offers a low impedance of 30 kΩ</i> from footnotes 2 and 3 in <i>Absolute Maximum Ratings</i> table • Changed test conditions of Input Overvoltage Protection Circuit, V_{OVP} parameter..... • Deleted multichannel reference from <i>Overview</i> section • Changed the <i>input voltage range</i> for each analog channel to the <i>input voltage range</i> in <i>Analog Input Structure</i> section .. • Changed <i>Input Overvoltage Protection Limits When AVDD = 5 V</i> table name from <i>Input Overvoltage Protection Limits When AVDD = 5 V or Offers a Low Impedance of 30 kΩ</i>..... • Changed <i>AVDD is floating with an impedance 30 kΩ</i> to <i>AVDD is floating</i> in <i>Input Protection Circuit</i> section..... • Changed <i>Input Overvoltage Protection Limits When AVDD = Floating</i> table title from <i>Input Overvoltage Protection Limits When AVDD = Floating with Impedance 30 kΩ</i>..... • Added footnotes to <i>List of Input Commands</i> table 			1 4 6 7 23 24 25 26 26 43
The datasheet number will be changing.			
Device Family		Change From:	Change To:
ADS8681, ADS8685, ADS8689		SBAS633B	SBAS633C
These changes may be reviewed at the datasheet links provided.			
http://www.ti.com/product/ADS8681			
Reason for Change:			
To accurately reflect device characteristics.			
Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):			
No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.			
Changes to product identification resulting from this PCN:			
None.			
Product Affected:			
ADS8681IPW	ADS8681IPWR	ADS8681IRUMR	ADS8681IRUMT
ADS8685IPW	ADS8685IPWR	ADS8689IPW	ADS8689IPWR

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
WW PCN Team	PCN_ww_admin_team@list.ti.com