



Customer Information Notification

201308007IU01

Issue Date: 15-Aug-2013
Effective Date: 16-Aug-2013

UPDATE

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QUALITY

Management Summary

For several RTC products we adapted the SPI-bus timing parameters to more realistic values. Additionally small other changes to the DS were done.

Change Category

Wafer Fab process	Assembly Process	Product Marking	Design
Wafer Fab materials	Assembly Materials	Electrical spec./Test coverage	Mechanical Specification
Wafer Fab location	Assembly Location	Test Location	Packing/Shipping/Labeling

SPI-bus timing parameters and other small adaptations for several Real Time Clocks

Information Notification

For the mentioned Real Time Clock (RTC) products we adapted the SPI bus timing in a way that is more realistic for the customer to fulfil. Details can be found in the attached new datasheets' "revision History" chapter. For the PCA2129T, we also added a frequency stability spec point just for room temperature (25°C).

Why do we issue this Information Notification

This is mainly an errata correction.

Identification of Affected Products

Product identification does not change

Update Information

In the original CIN we missed to mention that for the PCA2129T, we also added a frequency stability spec point just for room temperature (25°C).

Impact

no impact to the product's functionality anticipated.

Data Sheet Revision

A new datasheet will be issued

Disposition of Old Products

Existing inventory will be shipped until depleted

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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Customer Focus, Passion to Win.

NXP Quality Management Team.

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