

PCN Number:	20170818000	PCN Date:	Oct. 17, 2017
Title:	Datasheet for TPS65218		
Customer Contact:	PCN Manager	Dept:	Quality Services
Proposed 1st Ship Date:	Jan. 17, 2018		
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below.



TPS65218

SLDS206C –NOVEMBER 2014–REVISED AUGUST 2017

Changes from Revision B (February 2016) to Revision C

Page

• Changed adjustable output voltage range to show separate values for DCDC1, DCDC2, and DCDC3 in <i>Features</i> section	1
• Changed DCDC4 default from 1.0 A : to 1.6 A in <i>Features</i> section	1
• Changed DCDC4 adjustable output voltage minimum from 0.85 V : to 1.175 V in <i>Features</i> section	1
• Changed 5-V load switch VIN range minimum from 3.0 V : to 4 V in <i>Features</i> section	1
• Changed the Simplified Schematic	2
• Added updates to Description column in the Pin Functions table	7
• Changed input voltage for LS1 max value from 3.3 V : to 3.6 V in the <i>Recommended Operating Conditions</i> section	10
• Changed the maximum value for the input voltage for LS3 parameter in the <i>Recommended Operating Conditions</i> table	10
• Added individual output voltage values for DCDC1, DCDC2, DCDC3, DCDC4 in the <i>Recommended Operating Conditions</i> section	10
• Changed output voltage for DCDC5 from 1.0 V (MAX) and 1.1 V (MIN) : to 1 V (TYP) in the <i>Recommended Operating Conditions</i> section	10
• Changed output voltage for DCDC6 from 1.8 V (MAX) and 1.8 V (MIN) : to 1.8 V (TYP) in the <i>Recommended Operating Conditions</i> section	10
• Added additional voltage conditions on output current for DCDC4 in the <i>Recommended Operating Conditions</i> section	10
• Changed output current for DCDC5, DCDC6 max value from 10 mA : to 25 mA in the <i>Recommended Operating Conditions</i> section	10
• Changed output current max value for LS2 from 1000 mA : to 920 mA in the <i>Recommended Operating Conditions</i> section	10
• Added voltage conditions to output current for LS3 in the <i>Recommended Operating Conditions</i> section	10
• Deleted Note 2 in the <i>Electrical Characteristics</i> section	11
• Added SYS_BU subsection in the <i>Electrical Characteristics</i> section	11
• Changed first parameter of the INT_LDO subsection from V _{OUT} : to V _{INT_LDO} in the <i>Electrical Characteristics</i> section	11
• Added additional test conditions to t _{HOLD} in the <i>Electrical Characteristics</i> section	11
• Added typ value to INT_LDO C _{OUT} in the <i>Electrical Characteristics</i> section	11

• Changed test condition in V _{OH} from GPO2_CNF : to GPO2_BUF in the <i>Electrical Characteristics</i> section	19
• Added test conditions to V _{OL} for nPFO in the <i>Electrical Characteristics</i> section	19
• Added new Note 2 in the <i>Electrical Characteristics</i> section	20
• Added updates to <i>Overview</i> section	22
• Changed the Functional Block Diagram	23
• Added updates to <i>Power-Up Sequencing</i> section	24
• Added updates to <i>Power-Down Sequencing</i> section	26
• Added updates to <i>Supply Voltage Supervisor and Power Good (PGOOD)</i> section	28
• Added updates and changes to <i>Internal LDO (INT_LDO)</i> section	30
• Added updates and changes to <i>Current Limited Load Switches</i> section	31
• Changed Typical Application of Load Switch 2 figure in the <i>Load Switch 2 (LS2)</i> section	32
• Added updates to <i>Load Switch 3 (LS3)</i> section	32
• Added updates and changes to <i>UVLO</i> section	33
• Added updates to <i>Battery-Backup Supply Power-Path</i> section	36
• Added updates to <i>Push Button Input (PB)</i> section	41
• Added updates to <i>AC_DET Input (AC_DET)</i> section	43
• Changed Modes of Operation Diagram in <i>Device Functional Modes</i> section	47
• Changed description for Bit 5 in the STATUS Register Field Descriptions table	57
• Added updates to the CONFIG2 Register Field Descriptions table	64
• Changed description for Bits 2-0 in the SLEW Register Field Descriptions table	78
• Added updates to <i>Layout</i> section	98
• Added <i>Receiving Notification of Documentation Updates</i> section to <i>Device and Documentation Support</i> section ..	100
• Added the section	100
• Changed the <i>Electrostatic Discharge Caution</i> statement	100

The datasheet number will be changing.

Device Family	Change From:	Change To:
TPS65218	SLDS206B	SLDS206C

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/TPS65218>

Reason for Change:

To more accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

Electrical specification performance changes as indicated above.

Changes to product identification resulting from this PCN:

None.

Product Affected:

TPS65218B101PHPR	TPS65218B101PHPT	TPS65218B1PHPR	TPS65218B1PHPT
TPS65218B1RSLR	TPS65218B1RSLT		

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